

16 M PECHNOLOGY CENTER 2000 TES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

Toshiaki SHINOHARA, et al.

: EXAMINER: CHU, Chris C.

SERIAL NO: 09/534,043

FILED: March 24, 2000

: GROUP ART UNIT: 2815

FOR: SEMICONDUCTOR MODULE

AND INSULATING SUBSTRATE

THEREFOR

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

In response to the Official Action dated April 8, 2002, please amend the aboveidentified application as follows:

IN THE SPECIFICATION

Page 7, line 4, please insert the following paragraph:

Fig. 2A is a cross-sectional view of the semiconductor module shown as mounted on the external heat sink according to a preferred embodiment of the present invention;

Page 7, after line 6, please insert the following paragraph: